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Firoozeh Vakilzadeh

(Depositor's name)

Firoozeh Vakilzadeh

(Signature)

September 30, 2005

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/662,780	09/15/2003	Kazuko Inuzuka	81790.0296	1491

TITLE OF INVENTION: SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE OF FAST RANDOM CYCLE SYSTEM AND TEST METHOD THEREOF

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	10/03/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS		10/04/2005 CCHAU2 00000016 10662780	
PHAM, LY D	2827	365-191000		01 FC:1501 1400.00 OP 02 FC:8001 9.00 OP 03 FC:1504 300.00 OP	

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Hogan & Hartson, LLP

2 \_\_\_\_\_  
3 \_\_\_\_\_

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Kabushiki kaisha Toshiba

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent):  Individual  Corporation or other private group entity  Government

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b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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34,117

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